PATENT ABSTRACTS OF JAPAN

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(54) INTERFACE CIRCUIT

(57)Abstract:
PROBLEM TO BE SOLVED: To provide an interface circuit capable of accurately realizing data transfer regardless of a system configuration without increasing the power consumption or chip area of a system configuration device.
SOLUTION: A timing control circuit (5) for detecting the change point of transfer data (Dc), extracting an effective data window and deciding strobe timing at which the data is fetched according to the extracted effective window and a strobe clock generation circuit (6) for generating a strobe clock signal for data fetch under the control of the circuit (5) are provided in the interface circuit of a memory control unit (2).